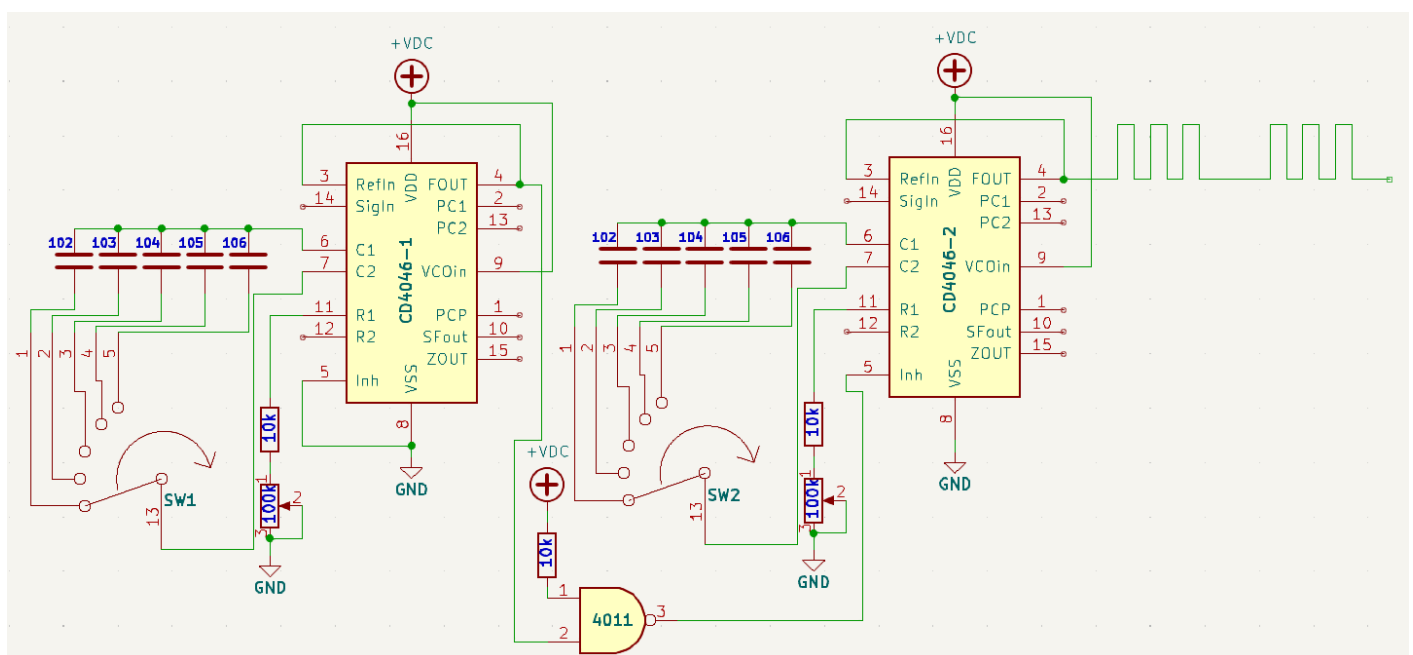


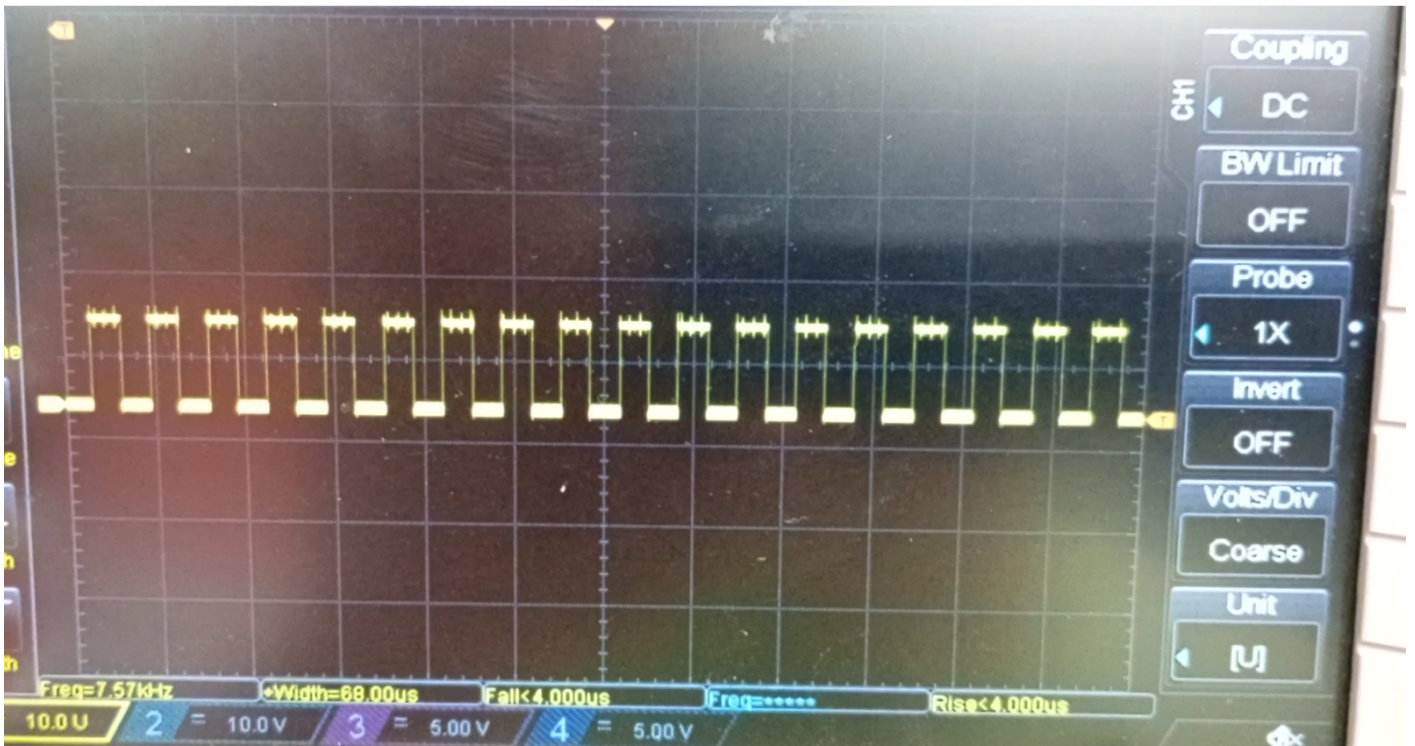
Circuit Overview

Utilizing two CD4046 Phase Lock Loop ICs and a CD4011 NAND gate to produce a gated pulse frequency waveform. One CD4046 output frequency is used to trigger the inhibit pin (pin #5) of the second CD4046, performing a "gating" function. Each frequency is based on adjustable RC time constants, frequency range being varied by selecting different capacitor values.

Circuit Schematic:



Duty cycle is fixed near 50% without the use of divide-by-ten counter ICs:



Resultant gated waveform with inhibit frequency superimposed:

